

JC720 U.S. PTO
10/035078



P

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10035078	12/28/2001	257	673	2826	Williams

****APPLICANTS:** Grigg Ford; Farnworth Warren;

****CONTINUING DATA VERIFIED:**

THIS APPLICATION IS A CON OF 09/569,216 05/11/2000 PAT 6,337,511

WHICH IS A DIV OF 09/176,967 10/22/1998 PAT 5,248,611

WHICH IS A CON OF 08/916,977 08/14/1997 PAT 5,840,598

**** FOREIGN APPLICATIONS VERIFIED:**

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☐ yes ☐ no

35 USC 119 conditions met ☐ yes ☐ no

Verified and Acknowledged Examiners's initials

ATTORNEY DOCKET NO

3134.7US (96-1171.7)

TITLE : LOC Semiconductor assembled with room temprature adhesive

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheeto Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
<p>WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.</p>			

FILED WITH:

☐ DISK (CRF)

☐ CD-ROM

(Attached in pocket on right inside flap)